

H.A

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/624,890	SADAKATA ET AL.	
	Examiner	Art Unit	
	Michael T. Tran	2827	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to communications dated December 18, 2003.
2.  The allowed claim(s) is/are 1-13.
3.  The drawings filed on 18 December 2003 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date \_\_\_\_\_.
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 12/15/03
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.



**MICHAEL TRAN**  
PRIMARY EXAMINER

## DETAILED ACTION

1. In response to the Communication dated December 18, 2003, claims 1-13 are active in this application.

### ***Foreign Priority***

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a) (d), which papers have been placed of record in the file.

### ***Information Disclosure Statement***

3. The information disclosure statement filed December 18, 2003 has been considered.

### ***Allowable Subject Matter***

4. Claims 1-13 are allowable over the prior art of record.
5. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to other elements in the claim) the following:
  - Means for setting the number of address signals in each of said plurality of RAMs equal to the number of address signals in the largest address space.

- Means for setting the number of address signals in each of said plurality of RAMs equal to the number of address signals in the largest address space when mapping the address space for test purposes.
- Means capable of changing an address connection between an external input address and a chip internal input address according to said differing memory spaces, wherein said means enables the number of address signals in each of said plurality of RAMs to be set equal to the number of address signals in the largest address space.
- Means for applying the same mapping to said differing memory spaces when simultaneously testing said plurality of RAMs; and means for inhibiting access to each individual RAM according to said differing memory spaces.
- Wherein all of said RAMs are tested simultaneously by setting the number of address signals in each of said plurality of RAMs equal to the number of address signals in the largest address space.

6. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the

Art Unit: 2827

examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795.

8. Any inquiry of a general nature or relating to the status of this application should be directed to Group receptionist whose telephone number is (571) 272-1650.



Michael T. Tran

February 4, 2005

**MICHAEL TRAN  
PRIMARY EXAMINER**